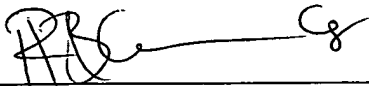
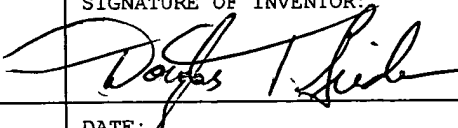



APPLICATION FOR UNITED STATES PATENT
DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I declare that my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor if only one name is listed below, or an original, first and joint inventor if plural inventors are named below, of the subject matter which is claimed and for which a patent is sought on the invention entitled as set forth below, and the title as set forth below which is described in the attached specification; that I have reviewed and understand the contents of the specification, including the claims, as amended by any amendment specifically referred to in the oath or declaration; that no application for patent or inventor's certificate on this invention has been filed by me or my legal representatives or assigns in any country foreign to the United States of America prior to the filing date of said application; and that I acknowledge my duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, section 1.56;

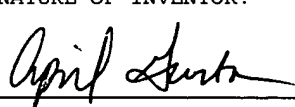
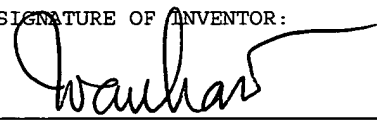
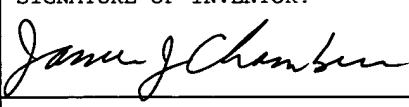
I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

TITLE OF INVENTION:		
RELIABLE HIGH VOLTAGE GATE DIELECTRIC LAYERS USING A DUAL NITRIDATION PROCESS		
POWER OF ATTORNEY: I HEREBY APPOINT THE FOLLOWING ATTORNEYS TO PROSECUTE THIS APPLICATION AND TRANSACT ALL BUSINESS IN THE PATENT AND TRADEMARK OFFICE CONNECTED THEREWITH Practitioners at Customer Number 23494		
SEND CORRESPONDENCE TO: Peter K. McLarty Texas Instruments Incorporated P.O. Box 655474, MS 3999 Dallas, TX 75265		DIRECT TELEPHONE CALLS TO: Peter K. McLarty (972) 917-4258
NAME OF INVENTOR: (1) Rajesh Khamankar	NAME OF INVENTOR: (2) Douglas T. Grider	NAME OF INVENTOR: (3) Hiroaki Niimi
RESIDENCE & POST OFFICE ADDRESS: 715 Beal Lane Coppell, Texas 75019	RESIDENCE & POST OFFICE ADDRESS: 2827 Summer Tree Lane McKinney, Texas 75070	RESIDENCE & POST OFFICE ADDRESS: 430 Buckingham Road, Apt. 2136 Richardson, Texas 75081
COUNTRY OF CITIZENSHIP: India	COUNTRY OF CITIZENSHIP: USA	COUNTRY OF CITIZENSHIP: Japan
SIGNATURE OF INVENTOR: 	SIGNATURE OF INVENTOR: 	SIGNATURE OF INVENTOR: 
DATE: June 19, 2003	DATE: June 20, 2003	DATE: June 10, 2003

APPLICATION FOR UNITED STATES PATENT
DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I declare that my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor if only one name is listed below, or an original, first and joint inventor if plural inventors are named below, of the subject matter which is claimed and for which a patent is sought on the invention entitled as set forth below, and the title as set forth below which is described in the attached specification; that I have reviewed and understand the contents of the specification, including the claims, as amended by any amendment specifically referred to in the oath or declaration; that no application for patent or inventor's certificate on this invention has been filed by me or my legal representatives or assigns in any country foreign to the United States of America prior to the filing date of said application; and that I acknowledge my duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, section 1.56;

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

TITLE OF INVENTION: RELIABLE HIGH VOLTAGE GATE DIELECTRIC LAYERS USING A DUAL NITRIDATION PROCESS		
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SEND CORRESPONDENCE TO: Peter K. McLarty Texas Instruments Incorporated P.O. Box 655474, MS 3999 Dallas, TX 75265		DIRECT TELEPHONE CALLS TO: Peter K. McLarty (972) 917-4258
NAME OF INVENTOR: (4) April Gurba	NAME OF INVENTOR: (5) Toan Tran	NAME OF INVENTOR: (6) James J. Chambers
RESIDENCE & POST OFFICE ADDRESS: 7817 Pennington Court Plano, Texas 75025	RESIDENCE & POST OFFICE ADDRESS: 2905 Lakeway Drive Rowlett, Texas 75088	RESIDENCE & POST OFFICE ADDRESS: 2501 Trailridge Drive Plano, Texas 75074
COUNTRY OF CITIZENSHIP: USA	COUNTRY OF CITIZENSHIP: USA	COUNTRY OF CITIZENSHIP: USA
SIGNATURE OF INVENTOR: 	SIGNATURE OF INVENTOR: 	SIGNATURE OF INVENTOR: 
DATE: June 20, 2003	DATE: June 20, 2003	DATE: JUNE 20, 2003